EPM7256AETI100-7N

Data Sheet

CPLD MAX 7000A Family 5K Gates 256 Macro Cells 126.6MHz 3.3V

Manufacturers	Altera Corporation (Intel)
Package/Case	TQFP-100
Product Type	Programmable Logic ICs
RoHS	Rohs
Lifecycle	

Images are for reference only

Please submit RFQ for EPM7256AETI100-7N or Email to us: sales@ovaga.com We will contact you in 12 hours. RFQ

General Description

EPM7256AETI100-7N is a specific model of Field-Programmable Gate Array (FPGA) manufactured by Intel Corporation (formerly Altera).

Features

The device has a capacity of 256 macrocells and 56 I/O pins.

It operates on a supply voltage of 3.3V and has a maximum frequency of 125 MHz.

It has a total of 6,144 programmable logic elements (LEs) and 18,432 bits of on-chip memory (RAM).

The FPGA is based on the MAX 7000 architecture and uses the In-System Programmable (ISP) technology for configuration.

Application

The EPM7256AETI100-7N can be used in a variety of applications such as digital signal processing, control systems, communication systems, and image processing.

It is also used in automotive, industrial, medical, and aerospace applications.

Due to its small form factor and low power consumption, it is ideal for portable and battery-operated devices.


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MAX®
EPM7256AETI100-7N
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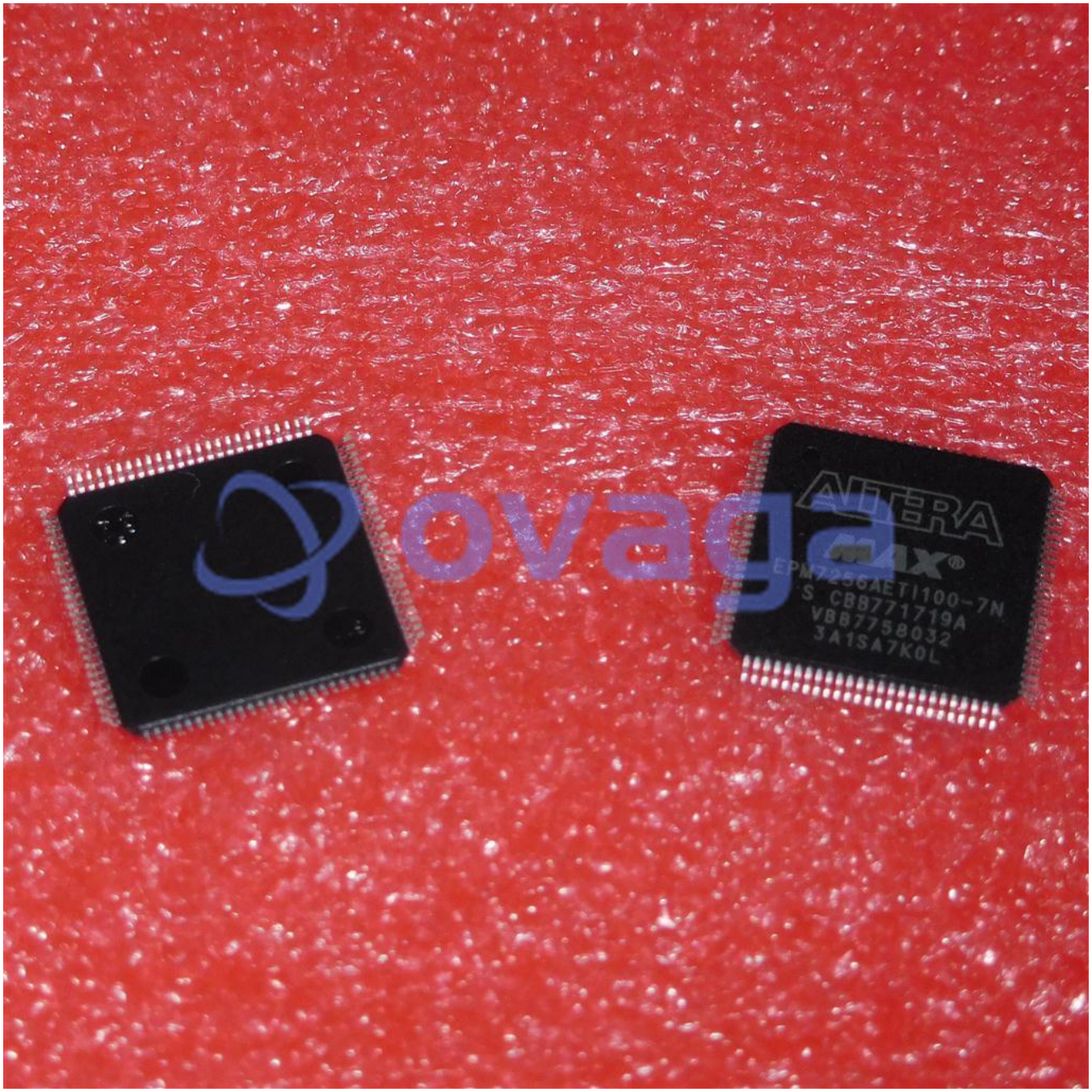
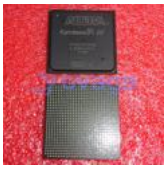


Related Products



[EP4CE55F29C8N](#)

Altera Corporation (Intel)

FBGA-780

[EPM240M100C5N](#)

Altera Corporation (Intel)

BGA-100

[EPM1270T144A5N](#)

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TQFP-144

[EPM570F256C5N](#)

Altera Corporation (Intel)

FBGA-256

EP2C35F672C8N

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FBGA-672

EPM7128AETC100-10

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TQFP-100

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FBGA-484

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FBGA-484