

AD5318BRUZ

Data Sheet

Digital to Analogue Converter.	10 bit.	167 kSPS.	SPI. 2.5V to	5.5V.	TSSOP.	16 Pins

Manufacturers	Analog Devices, Inc	parage .
Package/Case	TSSOP-16	
Product Type	Data Conversion ICs	mm
RoHS	Rohs	
Lifecycle		Images are for reference only

Please submit RFQ for AD5318BRUZ or Email to us: sales@ovaga.com We will contact you in 12 hours.

<u>RFQ</u>

General Description

The references for the eight DACs are derived from two reference pins (one per DAC quad). These reference inputs can be configured as buffered, unbuffered, or VDD inputs. The parts incorporate a power-on reset circuit, which ensures that the DAC outputs power up to 0 V and remain there until a valid write to the device takes place. The outputs of all DACs may be updated simultaneously using the asynchronous LDAC input. The parts contain a power-down feature that reduces the current consumption of the devices to 400 nA at 5 V (120 nA at 3 V). The eight channels of the DAC may be powered down individually.

All three parts are offered in the same pinout, which allows users to select the resolution appropriate for their application without redesigning their circuit board.

Features

Application

AD5308: 8 buffered 8-bit DACs in 16-lead TSSOP A version: ±1 LSB INL, B version: ±0.75 LSB INL	Portable battery-powered instruments		
AD5318: 8 huffered 10-bit DACs in 16-lead TSSOP A version: +4 I SB INI B version: +3 I SB INI	Digital gain and offset adjustment		
	Programmable voltage and current		
AD5328: 8 buffered 12-bit DACs in 16-lead TSSOP A version: ±16 LSB INL, B version: ±12 LSB INL	sources		
Low power operation: $0.7 \text{ mA} @ 3 \text{ V}$	Optical networking		
$\mathbf{D}_{\mathbf{A}} = \begin{bmatrix} 1 & 0 \\ 0 & 0 \end{bmatrix} = \begin{bmatrix} 1 & 0 \\ 0 & 0 \end{bmatrix} = \begin{bmatrix} 1 & 0 \\ 0 & 0 \end{bmatrix} = \begin{bmatrix} 1 & 0 \\ 0 & 0 \end{bmatrix} = \begin{bmatrix} 1 & 0 \\ 0 & 0 \end{bmatrix} = \begin{bmatrix} 1 & 0 \\ 0 & 0 \end{bmatrix} = \begin{bmatrix} 1 & 0 \\ 0 & 0 \end{bmatrix} = \begin{bmatrix} 1 & 0 \\ 0 & 0 \end{bmatrix}$	Automatic test equipment		
Power-down to 120 nA (a) 3 V, 400 nA (a) 5 V	Mobile communications		
Double-buffered input logic	Programmable attenuators		
Guaranteed monotonic by design over all codes	Industrial process control		
Buffered/unbuffered/VDD reference input options			
Output range: 0 V to VREF or 0 V to 2 VREF			
Power-on reset to 0 V			

ProgrammabilityIndividual channel power-downSimultaneous update of outputs (LDAC)

Low power, SPI-®, QSPI-TM, MICROWIRE-TM, and DSP-compatible 3-wire serial interface

Related Products



ADAS3022BCPZ Analog Devices, Inc LFCSP-40



AD574AJNZ Analog Devices, Inc PDIP-28



AD7938BSUZ Analog Devices, Inc



TQFP-32 AD7124-8BCPZ-RL7 Analog Devices, Inc

LFCSP-32





AD7401YRWZ Analog Devices, Inc

AD7266BSUZ

TQPF-32

SOIC-16

Analog Devices, Inc



Analog Devices, Inc TSSOP-24

AD7192BRUZ-REEL

AD9680BCPZ-500 Analog Devices, Inc LFCSP-64

Ovaga Technologies Limited