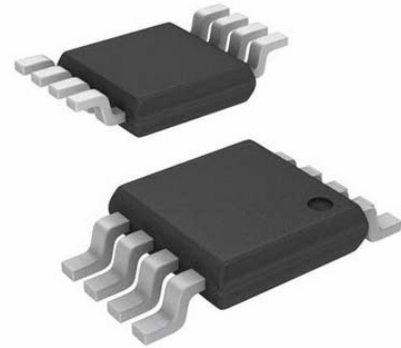


5PB1102 MIXED SIGNAL; HT SUSA CODE:8542390000

| | |
|---------------|---|
| Manufacturers | Renesas Technology Corp |
| Package/Case | TSSOP-8 |
| Product Type | Integrated Circuits (ICs) |
| RoHS | |
| Lifecycle | |



Images are for reference only

Please submit RFQ for 5PB1102PGGI8 or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The 5PB1102 is a high-performance 1:2 LVCMOS clock buffer. It has best-in-class Additive Phase Jitter of 50fsec RMS.

The 5PB1102 also supports an Output Enable function. It is available in 8-pin DFN and TSSOP packages and can operate from a 1.8V to 3.3V supply.

Features

High performance 1:2 LVCMOS clock buffer

Very low pin-to-pin skew <50ps

Very low additive jitter <50fs

Supply voltage: 1.8V to 3.3V

>
Integrated serial termination for 50ohm channel

Packaged in 8-pin TSSOP and small DFN packages

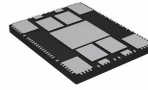
Extended (-40°C to +105°C) temperature range

Related Products



[HD74HC125P](#)

Renesas Technology Corp
DIP-14



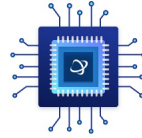
[5PB1213NTGK](#)

Renesas Technology Corp
VFQFPN-10



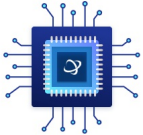
[5PB1106PGGI](#)

Renesas Technology Corp
TSSOP-14



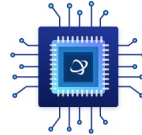
[5PB1106CMGI](#)

Renesas Technology Corp
COL-16



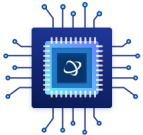
[5PB1102CMGI8](#)

Renesas Technology Corp
COL-8



[5PB1102CMGI](#)

Renesas Technology Corp
8-COL



[R5F64175PFB#UC](#)

Renesas Technology Corp



[R5S72645P144FPU](#)

Renesas Technology Corp
QFP-208