

74LVC02APW

Data Sheet

Quad 2-input NOR gate, Gates (AND / NAND / OR / NOR) 3.3V QUAD 2-INPUT NOR GATE

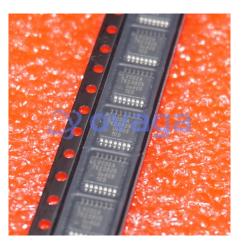
Manufacturers <u>NXP Semiconductor</u>

Package/Case TSSOP-14

Product Type Logic ICs

RoHS Rohs

Lifecycle



Images are for reference only

Please submit RFQ for 74LVC02APW or Email to us: sales@ovaga.com We will contact you in 12 hours.

RFO

General Description

74LVC02APW is a type of digital logic gate IC (integrated circuit) that belongs to the 74LVC series. Specifically, it is a quad 2-input NOR gate IC, meaning that it contains four separate NOR gates, each of which has two inputs and one output.

Features Application

Operating voltage range: 1.65V to 5.5V

Boolean logic circuits

Low power consumption Data processing systems

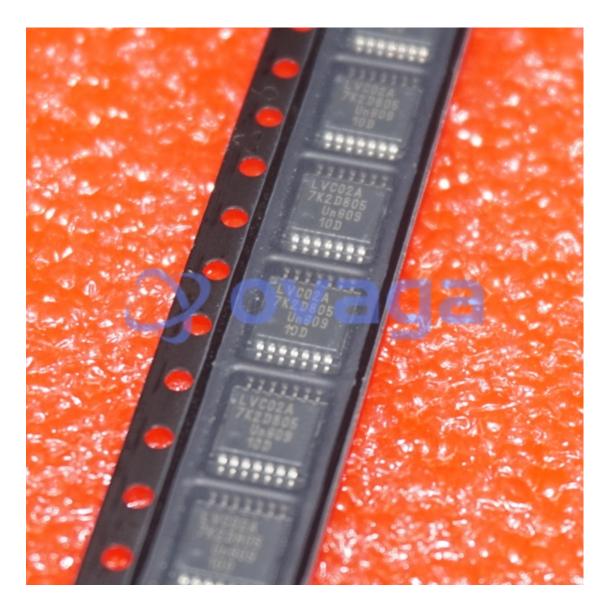
High-speed operation Interface circuits

Schmitt-trigger input for noise immunity Signal conditioning circuits

Balanced propagation delay and transition times Power management systems

14-pin TSSOP package Microcontrollers and microprocessors

Switching power supplies



Related Products



74HC4050D

NXP Semiconductor 16-SOIC



74HC132D

NXP Semiconductor SOP-14



74HC259D

NXP Semiconductor SOP-16



<u>74HC14D</u>

NXP Semiconductor SOP-14



74HC574D

NXP Semiconductor 20-SOIC



74HC165D

NXP Semiconductor SOP-16



74HCT02D

NXP Semiconductor SOP-14



74HC04D

NXP Semiconductor SOP-14