

50000 SYSTEM GATE 2.5 VOLT LOGIC CELL AR - NOT RECOMMENDED for NEW DESIGN FPGA

Manufacturers	AMD Xilinx, Inc
Package/Case	QFP208
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for XC2S50-5PQ208C or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

XC2S50-5PQ208C is a part number of a Field Programmable Gate Array (FPGA) device manufactured by Xilinx, which is a programmable logic IC that can be configured to perform a wide range of digital circuits.

Features

- 5ns maximum pin-to-pin delay
- 50,000 system gates
- 1.5Mb of fast block RAM
- 72 macrocells (configurable logic blocks)
- 2.5V power supply

Application

- Digital signal processing
- Communications systems
- Embedded systems
- Industrial control systems
- Medical devices
- Aerospace and defense

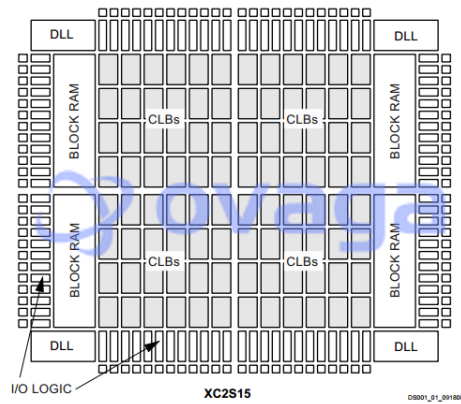


Figure 1: Basic Spartan-II Family FPGA Block Diagram

Related Products



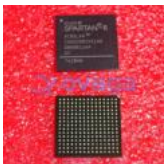
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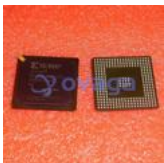
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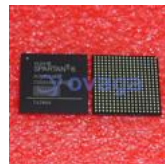
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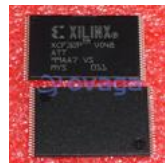
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