

X28HC256JI-15

Data Sheet

5 Volt, Byte Alterable EEPROM

Manufacturers	Renesas Technology Corp
Package/Case	PLCC32
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for X28HC256JI-15 or Email to us: sales@ovaga.com We will contact you in 12 hours.

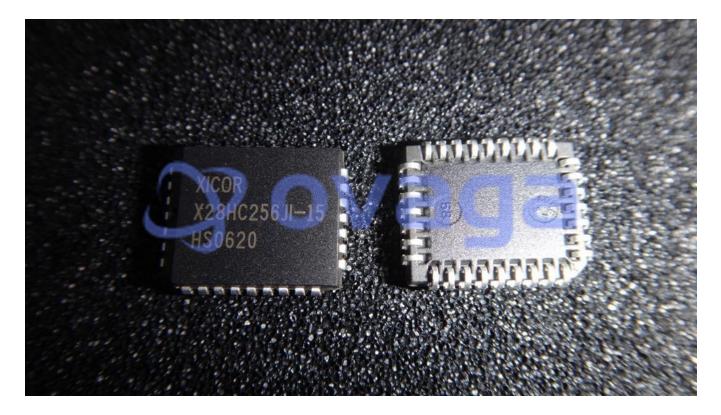
<u>RFO</u>

General Description

The X28HC256 is a second generation high performance CMOS 32k x 8 EEPROM. It is fabricated with Intersil's proprietary, textured poly floating gate technology, providing a highly reliable 5V only nonvolatile memory. The X28HC256 supports a 128-byte page write operation, effectively providing a 24µs/byte write cycle, and enabling the entire memory to be typically rewritten in less than 0.8s. The X28HC256 also featuresDATA polling and Toggle bit polling, two methods of providing early end of write detection. The X28HC256 also supports the JEDEC standard software data protection feature for protecting against inadvertent writes during power-up and power-down. Endurance for the X28HC256 is specified as a minimum 100,000 write cycles per byte and an inherent data retention of 100 years

Features

Access time: 90ns Simple byte and page write Single 5V supply No external high voltages or V_{P-P} control circuits Selftimed No erase before write No complex programming algorithms No overerase problem Low power CMOS Active: 60mA Standby: 500µA Software data protection Protects data against system level inadvertent writes High-Speed page write capability Highly reliable Direct Write[™] cell Endurance: 100,000 cycles Data retention: 100 years Early end of write detection DATA polling Toggle bit polling RoHS compliant



Related Products



X28HC64JZ-12

Renesas Technology Corp PLCC-32



X28HC256SI-12 Renesas Technology Corp SOP-28



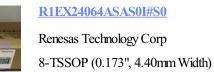
R1EX24016ASAS0I Renesas Technology Corp SOP8



R1EX24512ASAS0A Renesas Technology Corp SOP8



X28HC64JIZ-12 Renesas Technology Corp PLCC32





Renesas Technology Corp SOIC-8



R1EX24008ASAS0A Renesas Technology Corp SOP8