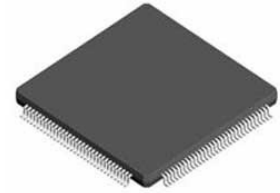


TW2867-QLC1-CR, Video Decoder NTSC, PAL, SECAM 5Channel 10Bit- 1.2V, 3.3V
128Pin LQFP

Manufacturers	Renesas Technology Corp
Package/Case	QFP-128
Product Type	Interface ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for TW2867-QLC1-CR or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The TW2867 includes four high quality NTSC/PAL/SECAM video decoders that convert analog composite video signal to digital component YCbCr data for security applications. Each channel contains 10 bit ADC and proprietary clamp and gain controllers and utilizes 4H comb filter for separating luminance & chrominance to reduce cross noise artifacts. The TW2867 adopts the image enhancement techniques, such as IF compensation filter, CTI and programmable peaking. The TW2867 also includes audio CODEC, which has five audio Analog-to-Digital converters and one Digital-to-Analog converter. A built-in audio controller can generate digital outputs for recording/mixing and accepts digital input for playback.

Features

Accepts all NTSC(M/4.43) / PAL(B/D/G/H/I/K/L/M/N/60)/SECAM standards with auto detection

Integrated four video analog anti-aliasing filters and 10 bit CMOS ADCs

High performance adaptive 4H comb filters for all NTSC/PAL standards

IF compensation filter for improvement of color demodulation

Color Transient Improvement (CTI)

Automatic white peak control

Programmable hue, saturation, contrast, brightness and sharpness

Proprietary fast video locking system for non-realtime application

Supports the standard ITU-R BT.656 format or time multiplexed output with 54/108MHz

Provides simultaneous four channel Full D1 and CIF time-multiplexed outputs with 54MHz.

Integrated five audio ADCs and one audio DAC

Provides multi-channel audio mixed analog output

Supports I2S/DSP Master/Slave interface for record output and playback input

PCM 8/16 bit and u-Law/A-Law 8bit for audio word length

Programmable audio sample rate that covers popular frequencies of 8/16/32/44.1/48kHz

Supports a two-wire serial host interface

Integrated clock PLL for 108MHz clock output.

Ultra low power consumption (Typical 431.88mW)

128 pin LQFP package

Related Products



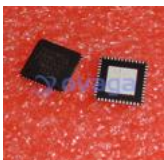
[TW9910-LB2-GR](#)

Renesas Technology Corp
LQFP-44



[TW2868-LA2-CR](#)

Renesas Technology Corp
TQFP128



[TW9910-NB2-GR](#)

Renesas Technology Corp
48-QFN



[TW2866-LC1-CR](#)

Renesas Technology Corp
QFP-128



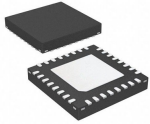
[TW9900-TA1-GR](#)

Renesas Technology Corp
QFP-32



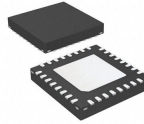
[TW9900-NA1-GR](#)

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WFQFN-32



[TW9990AT-NA1-GR](#)

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32pin- WFQFN