

23LC1024T-I/ST

Data Sheet

SRAM Chip Sync Dual 3.3V/5V 1M-bit 128K x 8 25ns 8-Pin TSSOP T/R

Manufacturers	Microchip Technology, Inc	
Package/Case	TSSOP-8	
Product Type	Memory	Ste Co
RoHS		
Lifecycle		Images are for reference only

Please submit RFQ for 23LC1024T-I/ST or Email to us: sales@ovaga.com We will contact you in 12 hours.

<u>RFQ</u>

General Description

The Microchip Technology Inc. 23A1024/23LC1024 are 1 Mbit Serial SRAM devices. The memory is accessed via a simple Serial Peripheral Interface (SPI) compatible serial bus. The bus signals required are a clock input (SCK) plus separate data in (SI) and data out (SO) lines. Access to the device is controlled through a Chip Select (CS) input. Additionally, SDI (Serial Dual Interface) and SQI (Serial Quad Interface) is supported if your application needs faster data rates. This device also supports unlimited reads and writes to the memory array. The 23A1024/23LC1024 is available in standard packages including 8-lead SOIC, PDIP and 8-lead TSSOP.

Related Products



23LCV1024-I/ST

Microchip Technology, Inc TSSOP-8



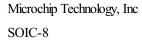
23LCV512-I/SN Microchip Technology, Inc SOIC-8



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23LCV1024-I/SN



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Microchip Technology, Inc SOIC-8

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23LC512-I/SN

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