

CPLD XC9500XL Family 6.4K Gates 288 Macro Cells 100MHz 0.35um (CMOS)
Technology 3.3V

Manufacturers	AMD Xilinx, Inc
Package/Case	QFP208
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for XC95288XL-10PQ208C or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

XC95288XL-10PQ208C is a Field-Programmable Gate Array (FPGA) manufactured by Xilinx Inc. It is a member of the XC9500XL family of FPGAs, which are known for their high performance, flexibility, and low power consumption.

Features

- 288 macrocells with up to 44 inputs each
- 4 global clock signals and 8 additional clock signals
- 5V tolerant I/O pins
- 10ns maximum pin-to-pin delay
- Internal 3.3V or 2.5V generation
- On-chip oscillator
- 208-pin PQFP package

Application

- Digital signal processing
- Embedded systems
- Networking
- Telecommunications
- Industrial control systems

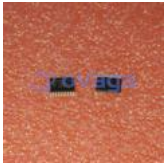


Related Products



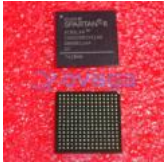
[XC18V01S020C](#)

AMD Xilinx, Inc
SOP-20



[XCF04SV0G20C](#)

AMD Xilinx, Inc
TSSOP20



[XC6SLX4-2CSG225C](#)

AMD Xilinx, Inc
BGA-225



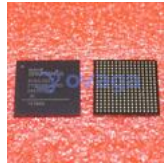
[XCV50-6BG256C](#)

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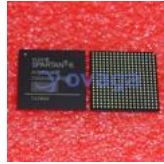
[XCF08PV0G48C](#)

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TSOP-48



[XC6SLX25-3FTG256C](#)

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[XC6SLX16-3CSG324C](#)

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BGA-324



[XCF32PVO48C](#)

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