

2 Output PCIe GEN1/2/3 Synthesizer; HT SUSA CODE:8542390000

Manufacturers	Renesas Technology Corp
Package/Case	16-VFQFN Exposed Pad
Product Type	Clock & Timer ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for 5V41235NLG8 or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The 5V41235 is a PCIe Gen2/3 compliant spread spectrum capable clock generator. The device has 2 differential HCSL outputs and can be used in communication or embedded systems to substantially reduce electro-magnetic interference (EMI). The spread amount and output frequency are selectable via select pins. The 5V41235 can also supply 25 MHz, 125 MHz and 200 MHz outputs for applications such as Ethernet.

Features

2 - 0.7V current mode differential HCSL output pairs

16-pin TSSOP and MLF packages; small board footprint

Spread-spectrum capable; reduces EMI

Outputs can be terminated to LVDS; can drive a wider variety of devices

25 MHz, 125 MHz and 200 MHz output frequencies; TSSOP only

100MHz and 200MHz output frequencies; MLF package

OE control pin; greater system power management

Spread% and frequency pin selection; no software required to configure device

Industrial temperature range available; supports demanding embedded applications

Cycle-to-cycle jitter < 100 ps

Output-to-output skew < 50 ps

PCIe Gen2 phase jitter < 3.0ps RMS

PCIe Gen3 phase jitter <1.0ps RMS

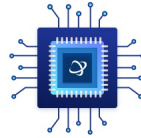


Related Products



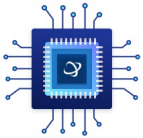
[5V41066PGG8](#)

Renesas Technology Corp
20-TSSOP (0.173, 4.40mm Width)



[5V41285PGGI8](#)

Renesas Technology Corp



[5V41285PGGI](#)

Renesas Technology Corp



[5V41236PGGI8](#)

Renesas Technology Corp
20-TSSOP (0.173, 4.40mm Width)



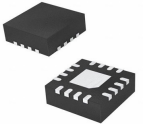
[5V41236PGGI](#)

Renesas Technology Corp
TSSOP-20



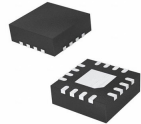
[5V41236PGG8](#)

Renesas Technology Corp
20-TSSOP (0.173, 4.40mm Width)



[5V41236NLGI8](#)

Renesas Technology Corp
20-VFQFN



[5V41236NLGI](#)

Renesas Technology Corp
20-VFQFN