

AD5381BSTZ-5

Data Sheet

40-Channel 12-Bit 3 V/5 V Single-Supply Voltage-Output DAC; Package: LQFP; No of Pins: 100; Temperature Range: Industrial

	Manufacturers	Analog Devices, Inc	and a second second
	Package/Case	LQFP-100	
	Product Type	Data Conversion ICs	Support of the suppor
	RoHS	Rohs	and the second sec
	Lifecycle		Images are for reference only
Please submit RFQ for AD5381BSTZ-5 or Email to us: sales@ovaga.com We will contact you in 12 hours.			

General Description

The AD5381 is a complete, single-supply, 40-channel, 12-Bit DAC available in a 100-lead LQFP package. All 40 channels have an on-chip output amplifier wiht rail-to-rail operation. The AD5381 includes a programmable internal 1.25 V/2.5 V, 10 ppm/°C reference, an on-chip channel monitor function that multiplexes the analog outputs to a common MON_OUT pin for external monitoring, and an output amplifier boost mode, which allows optimization of the amplifier slew rate. The AD5381 contains a double-buffered parallel interface featuring 20 ns WR pulse width, an SPI-/QSPI-/MICROWIRE-/DSP-compatible serial interface with interface speeds in excess of 30 MHz, and an I2C-compatible interface that supports a 400 kHz data transfer rate.

An input register followed by a DAC register provides double buffering, allowing the DAC outputs to be updated independently or simultaneously using the LDAC input.

Each channel has a programmable gain and offset adjust register that allows the user to fully calibrate any DAC chan-nel. Power consumption is typically 0.25 mA/channel with boost mode disabled.

Features

Guaranteed Monotonic

INL error: ±1 LSB max

On-Chip 1.25 V/2.5 V Reference With 10 ppm/°C TempCo

Temperature range: -40°C to +85°C

Rail-to-rail output amplifier

Power-down

Package type: 100-lead LQFP (14 mm × 14 mm)

User interfaces: Parallel Serial (SPI®-/QSPITM-/MICROWIRETM-/DSP-compatible, featuring data readback)

I2C®-compatible

Robust 6.5 kV HBM and 2 kV FICDM ESD rating

INTEGRATED FUNCTIONS

Channel monitor

Simultaneous output update via LDAC

Clear function to user-programmable code

Amplifier boost mode to optimize slew rate

User-programmable offset and gain adjust

Toggle mode enables square wave generation

Thermal monitors

Related Products



ADAS3022BCPZ Analog Devices, Inc LFCSP-40

AD574AJNZ Analog Devices, Inc PDIP-28



Analog Devices, Inc TQPF-32

AD7266BSUZ

AD7401YRWZ

Analog Devices, Inc SOIC-16

Application

Variable optical attenuators (VOAs)

Level setting (ATE)

Optical micro-electro-mechanical systems (MEMS)

Control systems

Instrumentation



AD7938BSUZ

Analog Devices, Inc TQFP-32



AD7192BRUZ-REEL

Analog Devices, Inc TSSOP-24



AD7124-8BCPZ-RL7

Analog Devices, Inc LFCSP-32



AD9680BCPZ-500

Analog Devices, Inc LFCSP-64