

CPLD XC9500XL Family 3.2K Gates 144 Macro Cells 100MHz 0.35um (CMOS)
Technology 3.3V

Manufacturers	AMD Xilinx, Inc
Package/Case	TQFP100
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for XC95144XL-10TQ100I or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

XC95144XL-10TQ100I is a Field-Programmable Gate Array (FPGA) manufactured by Xilinx Inc. The device has a capacity of 1,44,000 system gates and 144 macrocells, and operates on a 3.3V power supply. The "10" in the part number indicates that the device is rated for a maximum operating frequency of 10 MHz, and the "TQ100" indicates that it is housed in a 100-pin Thin Quad Flat Pack (TQFP) package.

Features

High-performance, low-power CMOS In-System Programmable (ISP) FPGA

System gates: 1,44,000

Macrocells: 144

Maximum user I/O pins: 114

Distributed RAM: 648 Kbits

Digital Clock Manager (DCM) blocks: 4

Maximum operating frequency: 10 MHz

3.3V power supply

Application

Digital signal processing

Communications

Aerospace and defense

Industrial automation

Medical devices

Test and measurement equipment

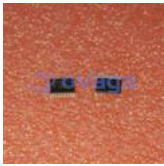


Related Products



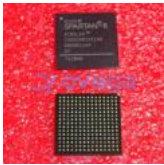
[XC18V01S020C](#)

AMD Xilinx, Inc
SOP-20



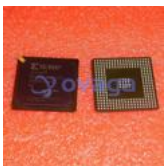
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AMD Xilinx, Inc
TSSOP20



[XC6SLX4-2CSG225C](#)

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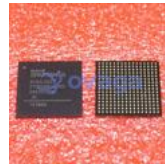
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BGA256



[XCF08PV0G48C](#)

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TSOP-48



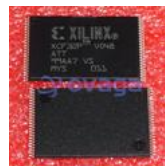
[XC6SLX25-3FTG256C](#)

AMD Xilinx, Inc
BGA-256



[XC6SLX16-3CSG324C](#)

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BGA-324



[XCF32PVO48C](#)

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TSOP48