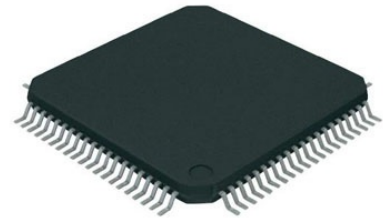


SRAM, 64 Kbit, 8K x 8bit, 3V to 3.6V, TQFP, 64 Pins, 20 ns New

Manufacturers	Renesas Technology Corp
Package/Case	TQFP-64
Product Type	Memory
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for 70V05L20PFGI or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The 70V05 is a high-speed 8K x 8 Dual-Port Static RAM designed to be used as a stand-alone 64K-bit Dual-Port SRAM or as a combination MASTER/SLAVE Dual-Port SRAM for 16-bit-or-more word systems which results in full speed, error-free operation without the need for additional discrete logic. An automatic power down feature controlled by CE permits the on-chip circuitry of each port to enter a very low standby power mode.

Features

True Dual-Ported memory cells which allow simultaneous reads of the same memory location

70V05 easily expands data bus width to 16 bits or more

Interrupt Flag

On-chip port arbitration logic

Full on-chip hardware support of semaphore signaling between ports

Fully asynchronous operation from either port

TTL-compatible, single 3.3V ($\pm 0.3V$) power supply

Available in 68-pin PGA and PLCC, and 64-pin TQFP packages

Industrial temperature range (-40C to +85C) is available

Related Products



[70V261L25PFGI](#)

Renesas Technology Corp
LQFP-100



[X28HC64SIZ-70](#)

Renesas Technology Corp
SOIC28



[70V28L20PFGI](#)

Renesas Technology Corp
TQFP-100



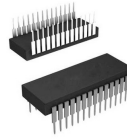
[70V27L15PFG](#)

Renesas Technology Corp
TQFP-100



[70V24L15PFG](#)

Renesas Technology Corp
100TQFP



[71256S70DB](#)

Renesas Technology Corp
28-CDIP (0.600, 15.24mm)



[70V28L15PFG](#)

Renesas Technology Corp
TQFP-100



[70V261L25PFG](#)

Renesas Technology Corp
TQFP-100