

SRAM Chip Async Dual 5V 8K-Bit 1K x 8 35ns 52-Pin PLCC

Manufacturers	Renesas Technology Corp
Package/Case	PLCC-52
Product Type	Memory
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for 7130SA35J or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

Features

True Dual-Ported memory cells which allow simultaneous access of the same memory location

On-chip port arbitration logic

BUSY output flag

INT flag for port-to-port communication

Fully asynchronous operation from either port

Battery backup operation—2V data retention (LA only)

TTL-compatible, single 5V $\pm 10\%$ power supply

Available in 48-pin DIP, LCC and Ceramic Flatpack, 52-pin PLCC, and 64-pin STQFP and TQFP packages

Industrial temperature range (-40C to $+85\text{C}$) is available



Related Products



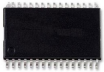
[71V016SA12PHG](#)

Renesas Technology Corp
SOP44



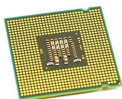
[71V416L10PHG](#)

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[DF2371RVLP34V](#)

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145-LGA



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SOJ-44



[71256S70DB](#)

Renesas Technology Corp
28-CDIP (0.600, 15.24mm)



[71321LA55PPGI](#)

Renesas Technology Corp
52-LQFP



[DF2371VLP34V](#)

Renesas Technology Corp
145-LGA