

XC2S200E-6PQG208I

YILINY 1

<u>RFO</u>

Data Sheet

200,000 SYSTEM GATE 1.8V FPGA - NOT RECOMMENDED for NEW DESIGN

Manufacturers	AMD Xilinx, Inc	
Package/Case	QFP208	
Product Type	Programmable Logic ICs	
RoHS		Images are for reference only
Lifecycle		
Please submit RFO	for XC2S200E-6PQG208I or Email to us; sales@	ovaga.com We will contact you in 12

General Description

hours.

LVDS.

XC2S200E-6PQG208I is a field-programmable gate array (FPGA) manufactured by Xilinx. Here are some of its features:

Features	Application
It has a total of 200,000 system gates.	XC2S200E-6FTG256C
It is based on the advanced 0.18-micron CMOS process technology.	XC2S200E-6PQG100C
It has 208 pins in a plastic quad flat pack (PQG208) package.	XC2S200E-6FGG256C
It has a maximum operating frequency of 250 MHz.	XC2S200E-6FTG256I
It has a total of 18,432 logic cells and 288 I/O pins.	XC2S200E-6FGG256I
It has 1.8V core voltage and supports a range of I/O standards, such as LVTTL, LVCMOS, HSTL, SSTL, and	



Related Products



XC18V01S020C AMD Xilinx, Inc

SOP-20



XCF04SV0G20C AMD Xilinx, Inc TSSOP20



XC6SLX4-2CSG225C AMD Xilinx, Inc

BGA-225



XCV50-6BG256C AMD Xilinx, Inc

BGA256



XCF08PV0G48C

AMD Xilinx, Inc TSOP-48

XC6SLX25-3FTG256C

AMD Xilinx, Inc BGA-256

XC6SLX16-3CSG324C

AMD Xilinx, Inc BGA-324

XCF32PVO48C

AMD Xilinx, Inc TSOP48