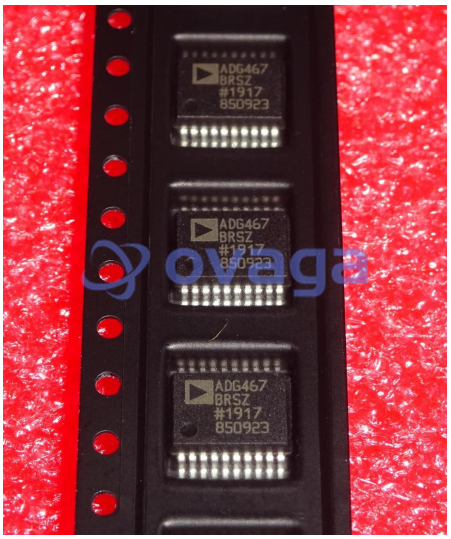
ADG467BRSZ-REEL

Data Sheet

Octal Channel Protector in SOIC Package; Package: SSOP; No of Pins: 20; Temperature Range: Industrial

Manufacturers	Analog Devices, Inc
Package/Case	SSOP-20
Product Type	Switches
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for ADG467BRSZ-REEL or Email to us: sales@ovaga.com We will contact you in 12 hours. RFQ

General Description

The ADG467 is an octal channel protector. The channelprotector is placed in series with the signal path. The channelprotector protects sensitive components from voltage transiencein the signal path regardless if the power supplies are present ornot. For this reason, the channel protectors are ideal for use inapplications where correct power sequencing cannot always beguaranteed (for example, hot insertion rack systems) to protectanalog inputs.

Each channel protector has an independent operation and consistsof an N-channel MOSFET, a P-channel MOSFET, and anN-channel MOSFET, connected in series. The channel protectorbehaves just like a series resistor during normal operation, thatis, (VSS + 1.5 V) < VIN < (VDD − 1.5 V). When a channel's analoginput exceeds the power supplies (including VDD and>

The ADG467 can operate off both bipolar and unipolarsupplies. The channels are normally on when power isconnected and open circuit when power is disconnected. Withpower supplies of ±15 V, the on resistance of the ADG467 is62 Ω typical with a leakage current of ±1 nA maximum. Whenpower is disconnected, the input leakage current is approximately±0.5 nA typical.

The ADG467 is available in an 18-lead SOIC package and a20-lead SSOP package.

Product Highlights

Fault Protection.The ADG467 can withstand continuous voltage inputsfrom −40 V to +40 V. When a fault occurs due to thepower supplies being turned off or due to an overvoltagebeing applied to the ADG467, the output is clamped.When power is turned off, current is limited to themicroampere level.

Low Power Dissipation.

Low RON. 62 Ω typical.

Trench Isolation Latch-Up Proof Construction.A dielectric trench separates the p- and n-channelMOSFETs thereby preventing latch-up.

Features

Fault and overvoltage protection up to ±40 V

Signal paths open circuit with power off

Signal path resistance of Ron with power on

44 V supply maximum ratings

Low on resistance: 62 Ω typical

Low RON match (5 Ω maximum)

Low power dissipation 0.8 µW typical

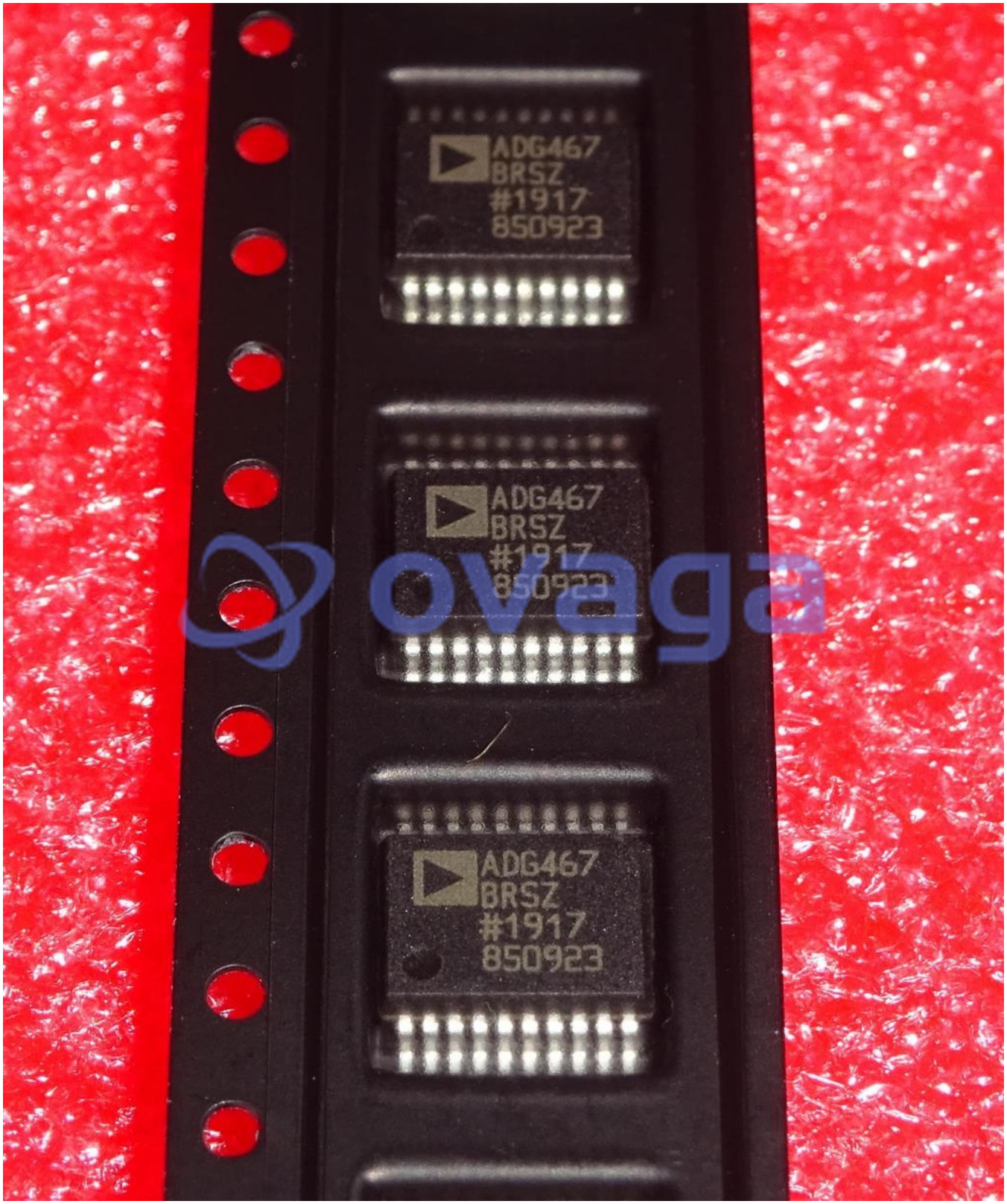
Latch-up proof construction

Application

ATE equipment

Sensitive measurement equipment

Hot insertion rack systems



Related Products

ADG467BRSZ

Analog Devices, Inc

SSOP-20

ADG201ABQ

Analog Devices, Inc

DIP-16

[ADP3050AR-3.3]

Analog Devices, Inc

SOP-8

[ADG512BR]

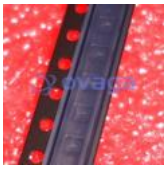
Analog Devices, Inc

SOP-16

[ADG888BCBZ-REEL7]

Analog Devices, Inc

16 ball WLCSP



[ADG852BCPZ-REEL7]

Analog Devices, Inc

LFCSP10

[ADG438FBNZ]

Analog Devices, Inc

DIP16

[ADG467BRZ]

Analog Devices, Inc

SOIC-18