

Charge Pump INV -1.5V to -7V 100mA 8-Pin SOIC N Tube

Manufacturers	<u>Analog Devices, Inc</u>
Package/Case	SOIC-8
Product Type	Power Management ICs
RoHS	Pb-free Halide free
Lifecycle	



Images are for reference only

Please submit RFQ for ADM8660ARZ or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

A Frequency Control (FC) input pin is used to select either 25 kHz or 120 kHz charge-pump operation. This is used to optimize capacitor size and quiescent current. With 25 kHz selected, a 10 μ F external capacitor is suitable, while with 120 kHz the capacitor may be reduced to 2.2 μ F. The oscillator frequency on the ADM660 can also be controlled with an external capacitor connected to the OSC input or by driving this input with an external clock. In applications where a higher supply voltage is desired it is possible to use the ADM660 to double the input voltage. With input voltages from 2.5 V to 7 V, output voltages from 5 V to 14 V are achievable with up to 100 mA output current.

The ADM8660 features a low power shutdown (SD) pin instead of the external oscillator (OSC) pin. This can be used to disable the device and reduce the quiescent current to 300 nA.

Features

ADM660: Inverts or Doubles Input Supply Voltage

ADM8660: Inverts Input Supply Voltage

100 mA Output Current

Shutdown Function (ADM8660)

2.2 μF or 10 μF Capacitors

0.3 V Drop at 30 mA Load

Low Power CMOS: 600 μA Quiescent Current

Selectable Charge Pump Frequency (25 kHz/120 kHz)

Pin Compatible Upgrade for MAX660, MAX665, ICL7660

Available in 16-Lead TSSOP Package

Application

Handheld Instruments

Portable Computers

Remote Data Acquisition

Op Amp Power Supplies



Related Products



[ADP3336ARMZ-REEL7](#)

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MSOP-8



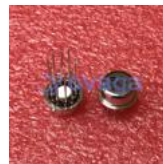
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