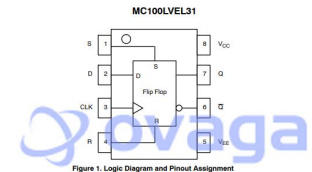


3.3V ECL D Flip-Flop with Set and Reset, Flip Flops 3.3V/5V ECL D-Type w/Set And Reset

Manufacturers	ON Semiconductor, LLC
Package/Case	TSSOP-8
Product Type	Logic ICs
RoHS	Rohs
Lifecycle	



PIN	FUNCTION
CLK	ECL Clock Input
Q, \bar{Q}	ECL Differential Data Outputs
D	ECL Data Input
R	ECL Reset Input
S	ECL Set Input
V _{CC}	Positive Supply
V _{EE}	Negative Supply

D	S	R	CLK	Q	\bar{Q}
L	L	L	Z	L	H
L	L	H	Z	H	L
L	H	L	X	Undef	Undef
L	H	H	X	Undef	Undef
H	L	L	X	Undef	Undef
H	L	H	X	Undef	Undef
H	H	L	X	Undef	Undef
H	H	H	X	Undef	Undef

Z = LOW to HIGH Transition
X = Don't Care

Images are for reference only

Please submit RFQ for MC100LVEL31DTG or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.



General Description

Application

ONSEMI

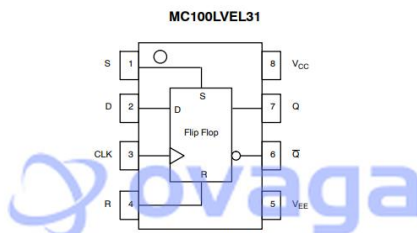


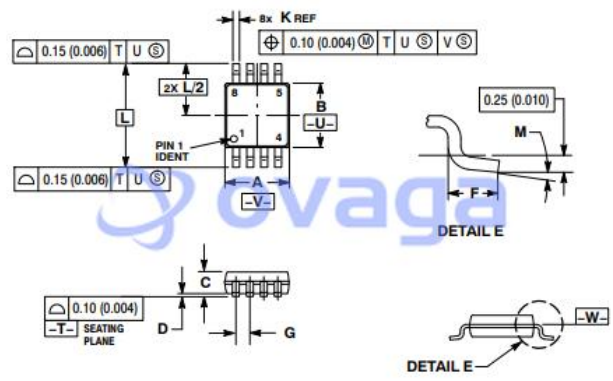
Table 1. PIN DESCRIPTION

PIN	FUNCTION
CLK	ECL Clock Input
Q, \bar{Q}	ECL Differential Data Outputs
D	ECL Data Input
R	ECL Reset Input
S	ECL Set Input
V _{CC}	Positive Supply
V _{EE}	Negative Supply

Table 2. TRUTH TABLE

D	S	R	CLK	Q	\bar{Q}
L	L	L	Z	L	H
L	L	H	Z	H	L
L	H	L	X	Undef	Undef
L	H	H	X	Undef	Undef
H	L	L	X	Undef	Undef
H	L	H	X	Undef	Undef
H	H	L	X	Undef	Undef
H	H	H	X	Undef	Undef

Z = LOW to HIGH Transition
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[MC74LCX16245DTG](#)

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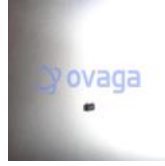
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