

## XC2S200-5FGG256C

Data Sheet

# 200000 SYSTEM GATE 2.5 VOLT LOGIC CELL A - NOT RECOMMENDED for NEW DESIGN

Manufacturers <u>AMD Xilinx, Inc</u>

Package/Case 256-BGA

Product Type Programmable Logic ICs

RoHS

Lifecycle



Images are for reference only

Please submit RFQ for XC2S200-5FGG256C or Email to us: sales@ovaga.com We will contact you in 12 hours.



### **General Description**

XC2S200-5FGG256C is a Field-Programmable Gate Array (FPGA) from Xilinx.

#### **Features**

It has 200,000 system gates.

It operates at a maximum frequency of 300 MHz.

It has 720 input/output (I/O) pins.

It has a configurable logic block (CLB) architecture, which allows for flexible implementation of logic functions.

It has built-in digital clock managers (DCMs) for clock management and jitter reduction.

It has a 1.2V core voltage and supports multiple I/O voltage standards.

## Application

XC2S200-5FGG256C can be used in a wide range of applications such as digital signal processing, video and image processing, high-speed communication, and networking.

It is also used in aerospace and defense, industrial automation, and medical equipment.



#### **Related Products**



XC18V01S020C

AMD Xilinx, Inc SOP-20



XCF04SV0G20C

AMD Xilinx, Inc TSSOP20



XC6SLX4-2CSG225C

AMD Xilinx, Inc BGA-225



<u>XCV50-6BG256C</u>

AMD Xilinx, Inc BGA256



XCF08PV0G48C

AMD Xilinx, Inc TSOP-48



XC6SLX25-3FTG256C

AMD Xilinx, Inc BGA-256



XC6SLX16-3CSG324C

AMD Xilinx, Inc BGA-324



XCF32PVO48C

AMD Xilinx, Inc TSOP48