

CPLD, 256 MACROCELL, 5.7NS, CPLD Type:-, No. of Macrocells:256, No. of I/O's:80, Supply Voltage Min:1.7V, Supply Voltage Max:1.9V



Images are for reference only

Manufacturers	<a href="#">AMD Xilinx, Inc</a>
Package/Case	100-TQFP
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	

Please submit RFQ for XC2C256-7VQG100C or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

## General Description

XC2C256-7VQG100C is an FPGA (Field-Programmable Gate Array) manufactured by Xilinx Inc. It is a member of the CoolRunner-II family of FPGAs, which are known for their low power consumption, high performance, and small footprint.

### Features

- Logic cells: 2560
- User I/Os: 98
- Block RAM: 72 Kbits
- Maximum user gate count: 40,960
- Maximum user flip-flops: 5,120
- Maximum user distributed RAM: 2,304 Kbits
- JTAG support for programming and debugging
- 7ns maximum pin-to-pin delay

### Application

- Digital signal processing
- Image processing
- Network processing
- Control systems
- Data acquisition
- Encryption and decryption
- Sensor interfacing



## Related Products



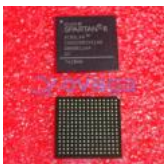
### [XC18V01S020C](#)

AMD Xilinx, Inc  
SOP-20



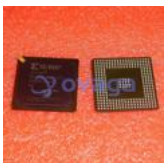
### [XCF04SV0G20C](#)

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TSSOP20



### [XC6SLX4-2CSG225C](#)

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BGA-225



### [XCV50-6BG256C](#)

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BGA256



### [XCF08PV0G48C](#)

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TSOP-48



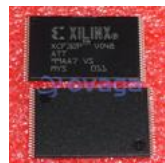
### [XC6SLX25-3FTG256C](#)

AMD Xilinx, Inc  
BGA-256



### [XC6SLX16-3CSG324C](#)

AMD Xilinx, Inc  
BGA-324



### [XCF32PVO48C](#)

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TSOP48