

XC95288XL High Performance CPLD

Manufacturers	AMD Xilinx, Inc
Package/Case	QFP208
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for XC95288XL-7PQ208I or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

XC95288XL-7PQ208I is a Field-Programmable Gate Array (FPGA) from the Xilinx CoolRunner XPLA3 family. It comes in a PQ208 package, operates with a speed grade of -7, and is RoHS compliant (no lead).

Features

- 288 macrocells
- 7,680 usable gates
- 12 input/output (I/O) pins
- 32 I/O pins that can be configured as input, output, or bidirectional
- 20 dedicated inputs
- 12 dedicated outputs
- 4 global clock signals
- JTAG boundary scan support for testing and programming
- Low-power operation with I/O options for 3.3V or 2.5V operation

Application

- Digital logic design and prototyping
- Embedded systems
- Industrial control and automation
- Communications and networking
- Military and aerospace applications
- Consumer electronics
- Automotive electronics

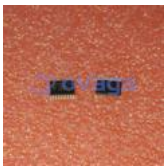


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