

FPGA Spartan -IIE Family 300K Gates 6912 Cells 357MHz 0.15um Technology 1.8V 208Pin PQFP

| | |
|---------------|---------------------------------|
| Manufacturers | AMD Xilinx, Inc |
| Package/Case | QFP208 |
| Product Type | Programmable Logic ICs |
| RoHS | |
| Lifecycle | |



Images are for reference only

Please submit RFQ for XC2S300E-6PQ208C or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

XC2S300E-6PQ208C is a model number of a field-programmable gate array (FPGA) manufactured by Xilinx. It is a member of the Spartan-II family of FPGAs and is designed to be used in various digital electronic circuits.

Features

- 300,000 system gates (equivalent to 2,400,000 bits of RAM)
- 300 user I/O pins
- Up to 622 I/O pins for high-speed differential signaling
- Up to 1,152 Kb of block RAM
- Support for various communication protocols such as PCI, Ethernet, USB, and others
- Built-in JTAG programming and boundary scan testing capability
- 6ns maximum pin-to-pin logic delay
- 208-pin plastic quad flat pack (PQFP) package
- Operating temperature range of -40°C to 85°C

Application

- Digital signal processing (DSP)
- Automotive applications
- Industrial control systems
- Aerospace and defense
- Communications equipment
- Medical equipment
- Consumer electronics

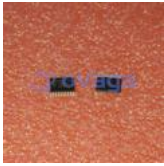


Related Products



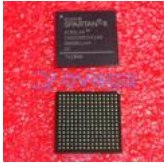
[XC18V01S020C](#)

AMD Xilinx, Inc
SOP-20



[XCF04SV0G20C](#)

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TSSOP20



[XC6SLX4-2CSG225C](#)

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BGA-225



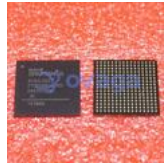
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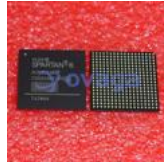
[XCF08PV0G48C](#)

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[XC6SLX25-3FTG256C](#)

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