

IC FIFO SS 32768X18 15NS 64-TQFP; HT SUSA CODE:8542320070

Manufacturers [Renesas Technology Corp](#)

Package/Case TQFP-64

Product Type Memory

RoHS

Lifecycle



Images are for reference only

Please submit RFQ for 72V275L15PF or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The 72V275 is a 32K x 18 SuperSync FIFO memory with clocked read and write controls. It's a functionally compatible version of the 72275 designed to run off a 3.3V supply for very low power consumption. The Frequency Select pin (FS) has been removed, thus it is no longer necessary to select which of the two clock inputs, RCLK or WCLK, is running at the higher frequency. SuperSync FIFOs are particularly appropriate for networking, video, telecommunications, data communications and other applications that need to buffer large amounts of data.

Features

Pin-compatible with the 72V2x5 SuperSync FIFOs

Functionally compatible with the 5 Volt 722x5 family

10ns read/write cycle time (6.5ns access time)

Fixed, low first word data latency time

Auto power down minimizes standby power consumption

Retransmit operation with fixed, low first word data latency time

Empty, Full and Half-Full flags signal FIFO status

Programmable Almost-Empty and Almost-Full flags, Easily expandable in depth and width

Independent Read and Write clocks (permit reading and writing simultaneously)

Available in 64-pin TQFP and STQFP packages

Industrial temperature range (-40C to +85C) is available



Related Products



[7205L15J](#)

Renesas Technology Corp
PLC32



[7204L25JI](#)

Renesas Technology Corp
32-LCC (J-Lead)



[72V36110L10PF](#)

Renesas Technology Corp
TQFP-100



[72V2113L10PFI](#)

Renesas Technology Corp
TQFP-80



[7201LA50TDB](#)

Renesas Technology Corp
CDIP28



[72V261LA10TEG](#)

Renesas Technology Corp
64-LQFP



[7204L12J](#)

Renesas Technology Corp
32-LCC (J-Lead)



[7207L20DB](#)

Renesas Technology Corp
CDIP-28