

1-of-2 Non-Inverting Demultiplexer with 3-State Deselected
Output, Encoders, Decoders, Multiplexers & Demultiplexers LOG 2:1 MUX WITH TRI-STATE

Manufacturers	ON Semiconductor, LLC
Package/Case	SC-70-6
Product Type	Logic ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for NL7SZ18DFT2G or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The NL7SZ18 is a high-performance non-inverting 1-to-2 demultiplexer. With the Select input (S) at Low, data at A is passed to Y0 and Y1 is set to high impedance. With the Select input (S) at High, data at A is passed to Y1 and Y0 is set to high impedance. The device operates over the voltage from 1.65 V to 5.5 V. This device has been optimized for on-board buffering applications and offers mixed (1.65 V, 2.3 V, 3.0 V and 5.5 V) voltage capability by providing over voltage tolerance (OVT) circuitry on I/O pins.

Features

High-Speed Propagation Delay tPD 2.5 nS (Typ), Load 50 pF @ 5.0 V

Power Down Impedance Outputs in High-Z

Output Drive Capability 4.0 mA to 32 mA

Broad VCC Operating Range 1.65V to 5.5 V

Surface Mount Technology SC-70, 6-Lead Packaging

OVT* on Inputs Permits 5.0 V to 3.0 V Translation

Pb-Free Package is Available

Application

ONSEMI

Related Products



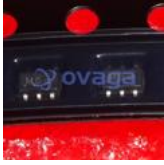
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