

5PB1102PGGI

Data Sheet

CLOCK BUFFER, 200MHZ, -40 TO 85DEG C New

Manufacturers Renesas Technology Corp

Package/Case TSSOP-8

Product Type Clock & Timer ICs, Clock Buffer

RoHS

Lifecycle



Images are for reference only

Please submit RFQ for 5PB1102PGGI or Email to us: sales@ovaga.com We will contact you in 12 hours.



General Description

The 5PB1102 is a high-performance 1:2 LVCMOS clock buffer. It has best-in-class Additive Phase Jitter of 50fsec RMS.

The 5PB1102 also supports an Output Enable function. It is available in 8-pin DFN and TSSOP packages and can operate from a 1.8V to 3.3V supply.

Features

High performance 1:2 LVCMOS clock buffer

Very low pin-to-pin skew <50ps

Very low additive jitter < 50fs

Supply voltage: 1.8V to 3.3V

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Integrated serial termination for 50ohm channel

Packaged in 8-pin TSSOP and small DFN packages

Extended (-40°C to +105°C) temperature range

Related Products



5P35021-000NDGI

Renesas Technology Corp 20-VFQFN



5PB1108PGGI

Renesas Technology Corp TSSOP-16



5PB1108CMGI

Renesas Technology Corp QFN-16



5P49V6901A000NLGI

Renesas Technology Corp



<u>5P49V6975A000LTGI</u>

Renesas Technology Corp 24-LFLGA



5P49V6965A000NLGI

Renesas Technology Corp 24-VFQFN



5P35023B-000NLGI

Renesas Technology Corp



<u>5P49V5925B000NLGI8</u>

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