

50,000 SYSTEM GATE 1.8V FPGA - NOT RECOMMENDED for NEW DESIGN

Manufacturers	<a href="#">AMD Xilinx, Inc</a>
Package/Case	BGA256
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for XC2S50E-6FTG256C or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.



## General Description

XC2S50E-6FTG256C is a Field-Programmable Gate Array (FPGA) from the Xilinx Spartan-2E family. Here are its features:

### Features

- Logic Cells: 3,888
- System Gates: 50,000
- Block RAM: 72 Kbits
- Digital Clock Managers (DCMs): 4
- Maximum Frequency: 200 MHz
- Package: 256-pin Fine-Pitch Ball-Grid Array (FBGA)

### Application

- XC2S50E-6PQ208C
- XC2S50-6TQ144C
- EP2S30F484C3N
- EP2S60F672C5N
- EP2S180F1020C5N



## Related Products



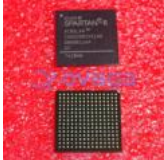
### [XC18V01S020C](#)

AMD Xilinx, Inc  
SOP-20



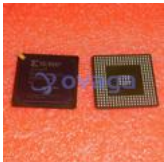
### [XCF04SV0G20C](#)

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### [XC6SLX4-2CSG225C](#)

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BGA-225



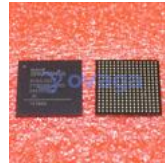
### [XCV50-6BG256C](#)

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BGA256



### [XCF08PV0G48C](#)

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### [XC6SLX25-3FTG256C](#)

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### [XC6SLX16-3CSG324C](#)

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BGA-324



### [XCF32PVO48C](#)

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