EPM3064ATC44-10N

Data Sheet

CPLD MAX® 3000A Family 1.25K Gates 64 Macro Cells 100MHz 3.3V 44-Pin TQFP Tray

Manufacturers	Altera Corporation (Intel)
Package/Case	TQFP-44
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	

Images are for reference only

Please submit RFQ for EPM3064ATC44-10N or Email to us: sales@ovaga.com We will contact you in 12 hours. RFQ

General Description

EPM3064ATC44-10N is an integrated circuit (IC) that belongs to the MAX 3000A family of Complex Programmable Logic Devices (CPLDs) designed by Altera Corporation, which is now part of Intel Corporation.

Features

It has a total of 64 macrocells (logic blocks) that can be programmed to implement custom digital logic functions.

The CPLD has a 10ns propagation delay, which determines the maximum speed at which it can operate.

It has a total of 34 input/output (I/O) pins that can be configured as either inputs or outputs.

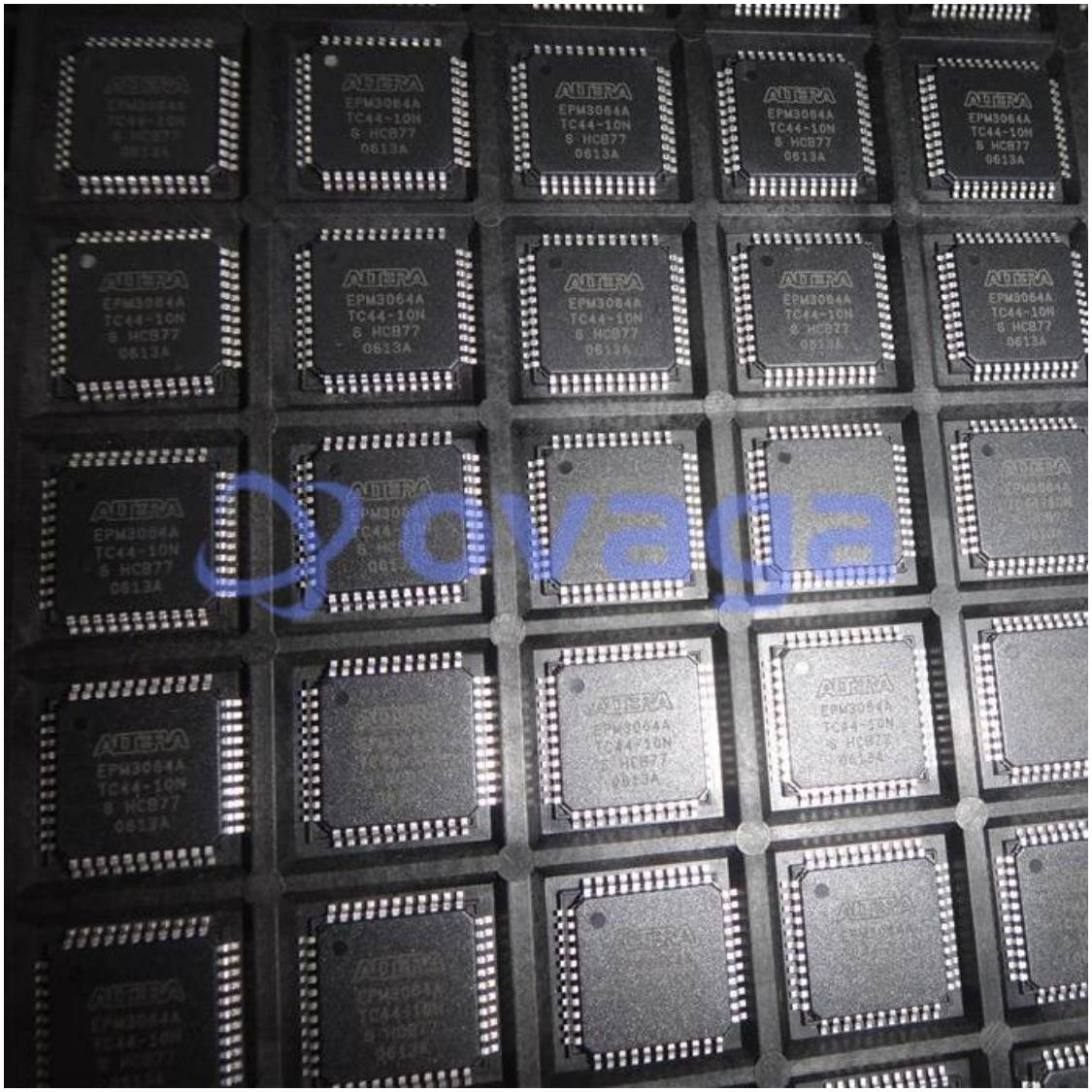
It has a 3.3V power supply requirement.

Application

EPM3064ATC44-10N is commonly used in digital systems for industrial, automotive, and communications applications where a high level of integration and flexibility is required.

It can be used to implement custom digital logic functions such as state machines, data buses, and arithmetic circuits.

It can be used to replace discrete logic circuits or standard logic devices such as TTL or CMOS gates.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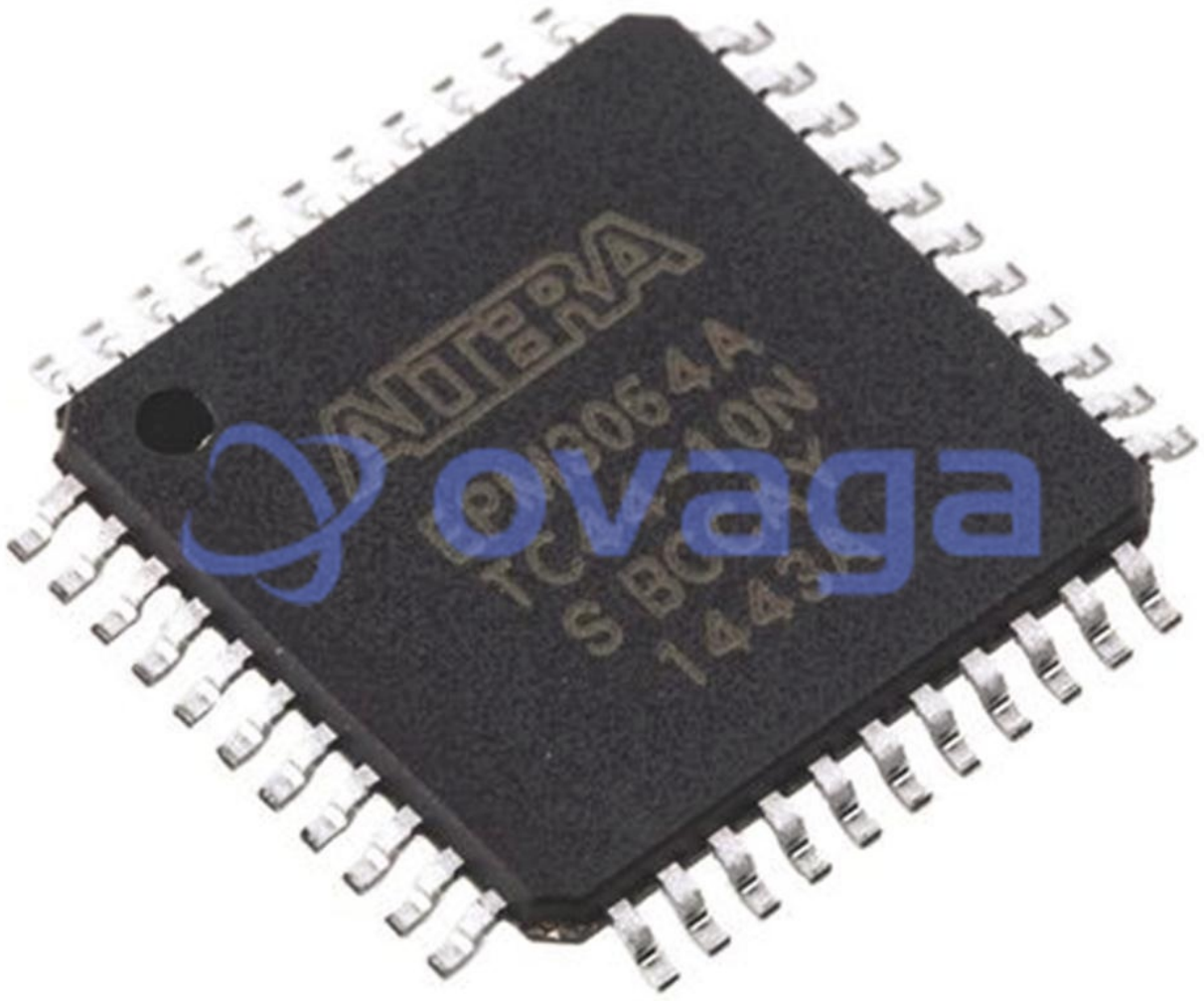
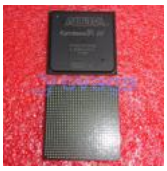


Related Products



[EP4CE55F29C8N](#)

Altera Corporation (Intel)

FBGA-780

[EPM240M100C5N](#)

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FBGA-484