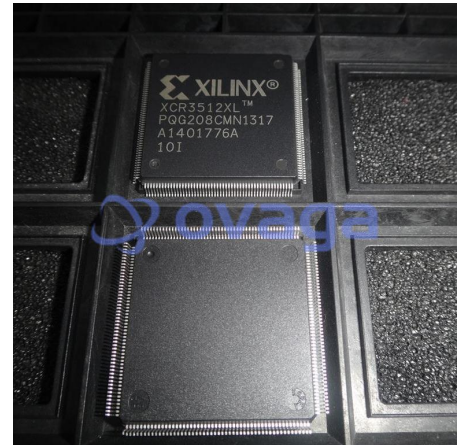


CPLD CoolRunner XPLA3 Family 12K Gates 512 Macro Cells 97MHz 0.35um (CMOS)
Technology 3.3V

Manufacturers	AMD Xilinx, Inc
Package/Case	208-BFQFP
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for XCR3512XL-10PQG208I or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

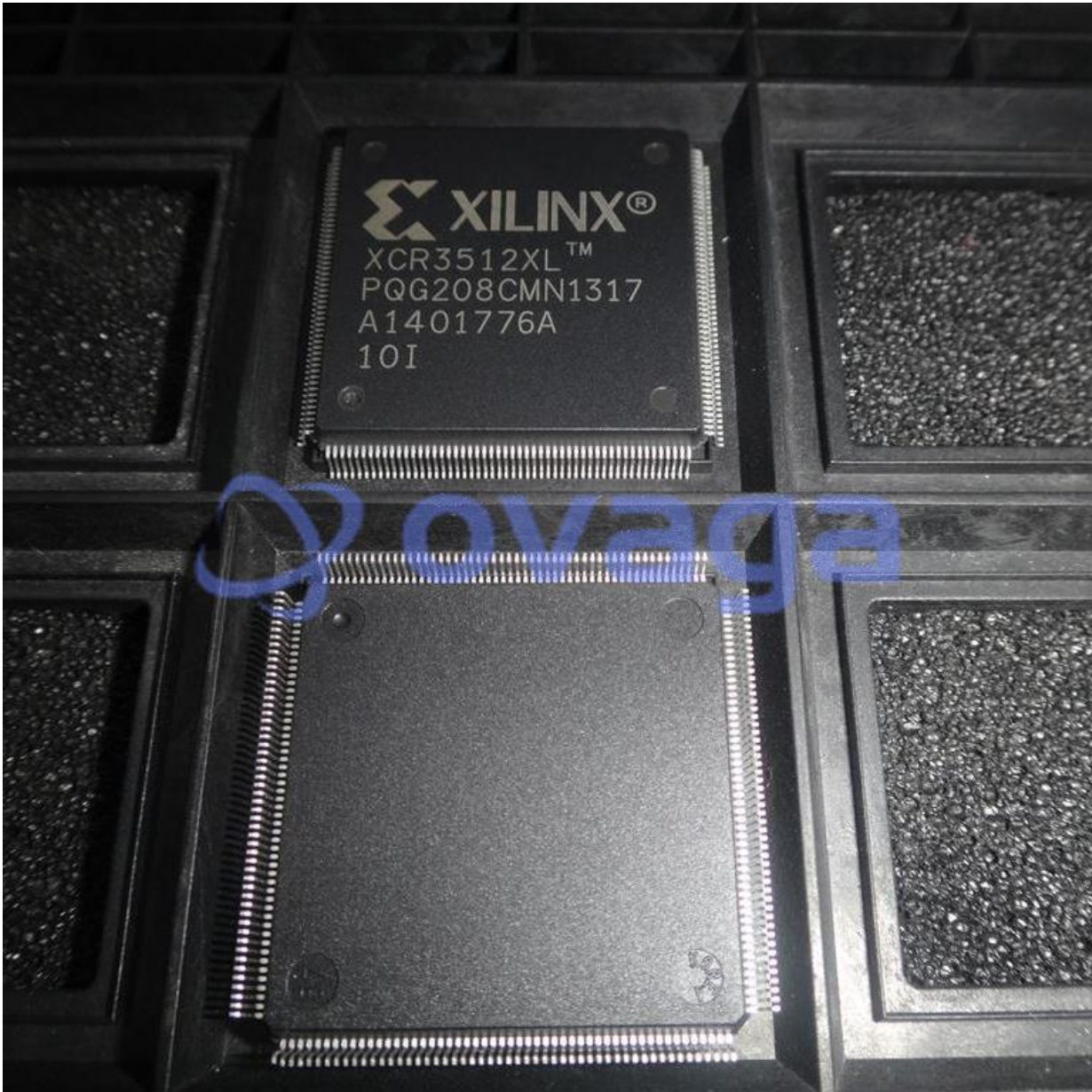
XCR3512XL-10PQG208I is a Field-Programmable Gate Array (FPGA) manufactured by Xilinx, a leading supplier of programmable logic devices.

Features

- It belongs to the Xilinx CoolRunner XPLA3 family of FPGAs
- It has 512 macrocells, which can be configured to implement a variety of digital logic functions
- It has 10ns pin-to-pin delay, which allows for high-speed operation
- It operates with a 1.8V-3.3V power supply
- It has 208 pins in a PQG208 package.

Application

- Industrial automation and control
- Communication systems
- Embedded systems
- Video and image processing
- Military and aerospace applications
- Automotive systems



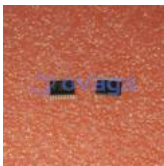


Related Products



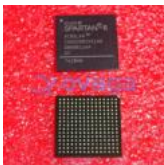
[XCR18V01S020C](#)

AMD Xilinx, Inc
SOP-20



[XCF04SY0G20C](#)

AMD Xilinx, Inc
TSSOP20



[XC6SLX4-2CSG225C](#)

AMD Xilinx, Inc
BGA-225



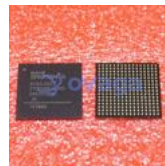
[XCV50-6BG256C](#)

AMD Xilinx, Inc
BGA256



[XCF08PV0G48C](#)

AMD Xilinx, Inc
TSOP-48



[XC6SLX25-3FTG256C](#)

AMD Xilinx, Inc
BGA-256



[XC6SLX16-3CSG324C](#)

AMD Xilinx, Inc
BGA-324



[XCF32PVO48C](#)

AMD Xilinx, Inc
TSOP48