

CPLD CoolRunner XPLA3 Family 1.5K Gates 64 Macro Cells 95MHz 0.35um (CMOS)
Technology 3.3V

Manufacturers	AMD Xilinx, Inc
Package/Case	QFP100
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for XCR3064XL-10VQG100I or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

XCR3064XL-10VQG100I is a Field Programmable Gate Array (FPGA) manufactured by Xilinx, a leading company in the FPGA industry.

Features

- 64 macrocells with 64 flip-flops each
- 3,000 usable gates
- 64 input/output pins
- Programmable power-saving mode
- In-system programmable

Application

- Digital signal processing
- Communication systems
- Industrial automation
- Robotics
- Medical devices
- Automotive systems



Related Products



[XC18V01S020C](#)

AMD Xilinx, Inc
SOP-20



[XCF04SV0G20C](#)

AMD Xilinx, Inc
TSSOP20



[XC6SLX4-2CSG225C](#)

AMD Xilinx, Inc
BGA-225



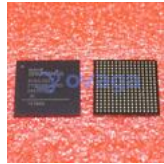
[XCV50-6BG256C](#)

AMD Xilinx, Inc
BGA256



[XCF08PV0G48C](#)

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TSOP-48



[XC6SLX25-3FTG256C](#)

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BGA-256



[XC6SLX16-3CSG324C](#)

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[XCF32PVO48C](#)

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