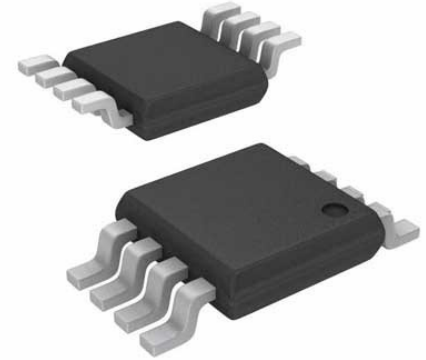


2.5 V to 5.5 V, 250 μ A, 2-Wire Interface, Dual Voltage Output, 8-Bit DACs; Package: MSOP; No of Pins: 8; Temperature Range: Industrial



Images are for reference only

Manufacturers	Analog Devices, Inc
Package/Case	MSOP-8
Product Type	Data Conversion ICs
RoHS	Rohs
Lifecycle	

Please submit RFQ for AD5337ARMZ or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The AD5337/AD5338/AD5339 are dual 8-, 10-, and 12-bit buffered voltage output DACs, respectively. Each part is housed in an 8-lead MSOP package and operates from a single 2.5 V to 5.5 V supply, consuming 250 μ A at 3 V. On-chip output amplifiers allow rail-to-rail output swing with a slew rate of 0.7 V/ μ s. A 2-wire serial interface operates at clock rates up to 400 kHz. This interface is SMBus compatible at $V_{DD} < 3.6$ V. Multiple devices can be placed on the same bus.

The references for the two DACs are derived from one reference pin. The outputs of all DACs can be updated simultaneously using the software LDAC function. The parts incorporate a power-on reset circuit to ensure that the DAC outputs power up to 0 V and remain there until a valid write to the device takes place. A software clear function resets all input and DAC registers to 0 V. A power-down feature reduces the current consumption of the devices to 200 nA @ 5 V (80 nA @ 3 V).

The low power consumption of these parts in normal operation makes them ideally suited to portable battery-operated equipment. The power consumption is typically 1.5 mW at 5 V and 0.75 mW at 3 V, reducing to 1 μ W in power-down mode.

Features

AD5337: 2 buffered 8-bit DACs in 8-lead MSOP

AD5338, AD5338-1: 2 buffered 10-bit DACs in 8-lead MSOP

AD5339: 2 buffered 12-bit DACs in 8-lead MSOP

Low power operation: 250 μ A @ 3 V, 300 μ A @ 5 V

2-wire (I2C-compatible) serial interface

2.5 V to 5.5 V power supply

Guaranteed monotonic by design over all codes

Power-down to 80 nA @ 3 V, 200 nA @ 5 V

3 power-down modes

Double-buffered input logic

Output range: 0 V to VREF

Power-on reset to 0 V

Application

Portable battery-powered instruments

Digital gain and offset adjustment

Programmable voltage and current sources

Programmable attenuators

Industrial process control

Related Products



[ADAS3022BCPZ](#)

Analog Devices, Inc
LFCSP-40



[AD574AJNZ](#)

Analog Devices, Inc
PDIP-28



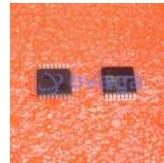
[AD7938BSUZ](#)

Analog Devices, Inc
TQFP-32



[AD7124-8BCPZ-RL7](#)

Analog Devices, Inc
LFCSP-32



[AD7266BSUZ](#)

Analog Devices, Inc
TQFP-32



[AD7401YRWZ](#)

Analog Devices, Inc
SOIC-16



[AD7192BRUZ-REEL](#)

Analog Devices, Inc
TSSOP-24



[AD9680BCPZ-500](#)

Analog Devices, Inc
LFCSP-64